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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/724,851	12/01/2003	Robert N.C. Broberg III	03-1458/L13.12-0245	2165
7590		05/07/2007		
Leo J. Peters LSI Logic Corporation MS D-106 1621 Barber Lane Milpitas, CA 95035			EXAMINER TECKLU, ISAAC TUKU	
			ART UNIT 2192	PAPER NUMBER
			MAIL DATE 05/07/2007	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/724,851

Applicant(s)

BROBERG ET AL.

Examiner

Isaac T. Tecklu

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 01 December 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

1. This action is responsive to the application filed on 12/01/2003.
2. Claims 1-20 have been examined.

Oath/Declaration

3. The office acknowledges receipt of a properly signed oath/declaration filed on 12/01/2003.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Sharma et al. (US 5,841,663), hereinafter Sharma.

As per claim 1, Sharma discloses a process of abstracting file paths for a plurality of design files in a computer readable language comprising steps of (e.g. FIGURE 16, HDL Circuit Description 28 and related text):

- a) generating a description file defining file paths to the design files in a first environment (e.g. FIGURE 28, Parameterized HDL library Module 300A-300N and related text);
- b) parsing the description file to identify file paths to each of the design files (e.g. FIGURE 28, 32 and related text); and
- c) generating an index correlating each description file and its respective file path for the first environment (e.g. FIGURE 28, Intermediate RTL Description 34 and related text).

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As per claim 2, Sharma discloses the process of claim 1, wherein step (b) comprises:
b1) defining a directory of description files defining file paths in the first environment (e.g. FIGURE 7, Library Directory 80 and related text), and
b2) parsing the directory (col. 24: 47-60 "... each library are parsed with parser ...").

As per claim 3, Sharma discloses the process of claim 2, further comprises: d) defining a file path in a second environment for each description file (col. 8:10-20 "... datapath portion ..."), e) applying the index to the file paths in the second environment to define full file paths for each design file through the first and second environments (col. 12:30-35 "... RTL descriptions are mapped ..."), and f) applying the design files to the second environment using the full file paths (col. 18: 52-60 "... permit the creation of customized components ...").

As per claim 4, Sharma discloses the process of claim 3, further comprising: g) before step f), constructing a list containing design file names and respective full paths through the first and second environments (e.g. FIGURE 1, 72 and related text).

As per claim 5, Sharma discloses the process of claim 3, wherein step e) comprises: combining file paths in the index with respective file paths in the second environment (e.g. FIGURE 1, 78 and related text).

As per claim 6, Sharma discloses the process of claim 1, further comprises: d) defining a file path in a second environment for each description file (col. 8:10-20 "... datapath portion ..."), e) applying the index to the file paths in the second environment to define full file paths for each design file through the first and second environments (col. 12:30-35 "... RTL descriptions are mapped ..."), and f) applying the design files to the second environment using the full file paths (col. 18: 52-60 "... permit the creation of customized components ...").

As per claim 7, Sharma discloses the process of claim 6, further comprising g) before step f), constructing a list containing design file names and respective full paths through the first and second environments (e.g. FIGURE 1, 72 and related text).

As per claim 8, Sharma discloses the process of claim 6, wherein step e) comprises: combining file paths in the index with respective file paths in the second environment (e.g. FIGURE 1, 78 and related text).

As per claim 9, Sharma discloses the process of claim 8, wherein the combining step comprises concatenating the respective file paths (col. 26:50-55 "... compiled netlist can be combined ...").

As per claim 10, this is the process for applying a design file version of the claimed process discussed above (Claims 1 and 3), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Sharma.

As per claim 11, this is the process for applying design file version of the claimed process discussed above (Claim 4), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Sharma.

As per claim 12, this is the process for applying design file version of the claimed process discussed above (Claim 5), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Sharma.

As per claim 13, this is the computer usable medium version of the claimed process discussed above (Claim 1), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Sharma.

As per claim 14, this is the computer usable medium version of the claimed process discussed above (Claim 2), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Sharma.

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As per claim 15, this is the computer usable medium version of the claimed process discussed above (Claim 3), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Sharma.

As per claim 16, this is the computer usable medium version of the claimed process discussed above (Claim 4), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Sharma.

As per claim 17, this is the computer usable medium version of the claimed process discussed above (Claim 5), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Sharma.

As per claim 18, this is the computer usable medium version of the claimed process discussed above (Claim 6), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Sharma.

As per claim 19, this is the computer usable medium version of the claimed process discussed above (Claim 7), wherein all claim limitations have been addressed and/or covered in cited areas as set forth above. Thus, accordingly, these claims are also anticipated by Sharma.

Conclusion


6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Isaac T. Tecklu whose telephone number is (571) 272-7957. The examiner can normally be reached on M-TH 9:300A - 8:00P.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam can be reached on (571) 272-3695. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Isaac Tecklu
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TUAN DAM
SUPERVISORY PATENT EXAMINER